

FIG. 1

PRIOR ART

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

2
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ADDRESS	DATA	
TIME SLOT INFORMATION	CH DATA INFORMATION	
0 0 0 0	0 0 0 0	
0 0 0 1	0 0 0 1	
0 0 0 2	0 0 0 2	
0 0 0 3	0 0 0 3	
0 0 0 4	0 1 0 0	* 1
0 0 0 5	0 0 0 5	
}	}	
0 0 9 9	0 0 9 9	
0 1 0 0	0 0 0 4	* 2
0 1 0 1	0 1 0 1	

FIG. 3

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FIG. 4A
FRAME
SYNCHRONIZATION
SIGNAL b
-0.5CK

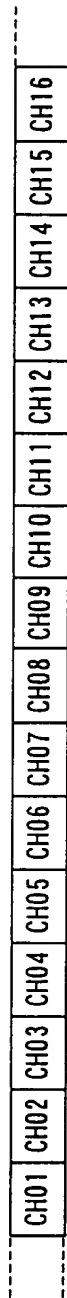


FIG. 4B
INPUT DATA Di



FIG. 4C
CLOCK a

APPROVED	O.G. FIG.	
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2 a
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ADDRESS		DATA
CH NUMBER	TIME SLOT NUMBER	TIME SLOT DATA
—	0 0 0 0	0 0 0 0
0 1	0 0 0 1	0 0 0 1
	0 0 0 2	0 0 0 2
	}	}
0 2	0 0 0 n	0 0 0 n
	0 0 0 n+1	0 0 0 n+1
	}	}
{	0 0 0 n+n	0 0 0 n+n
	}	}

FIG. 5

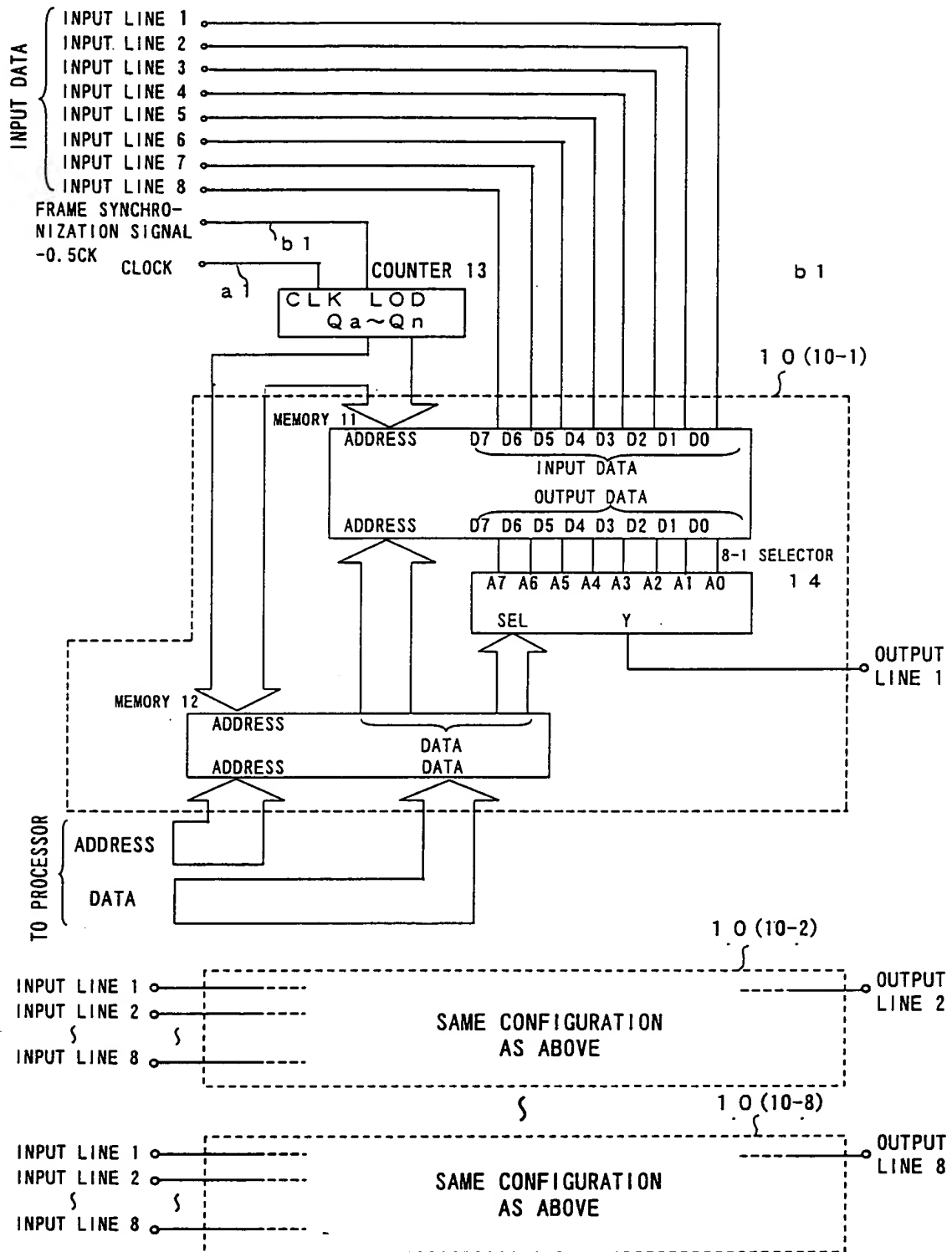


FIG. 6

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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12



ADDRESS	DATA	
TIME SLOT INFORMATION	(CH DATA INFORMATION)	(LINE INFORMATION)
0 0 0 0	0 0 0 0	0 0
0 0 0 1	0 0 0 1	0 0
0 0 0 2	0 0 0 2	0 0
0 0 0 3	0 0 5 0	0 3
0 0 0 4	0 1 0 0	0 0
0 0 0 5	0 0 0 5	0 0
}	}	}
0 0 9 9	0 1 0 0	0 7
0 1 0 0	0 0 0 4	0 0
0 1 0 1	0 1 0 1	0 0

* 3

* 4

FIG. 7

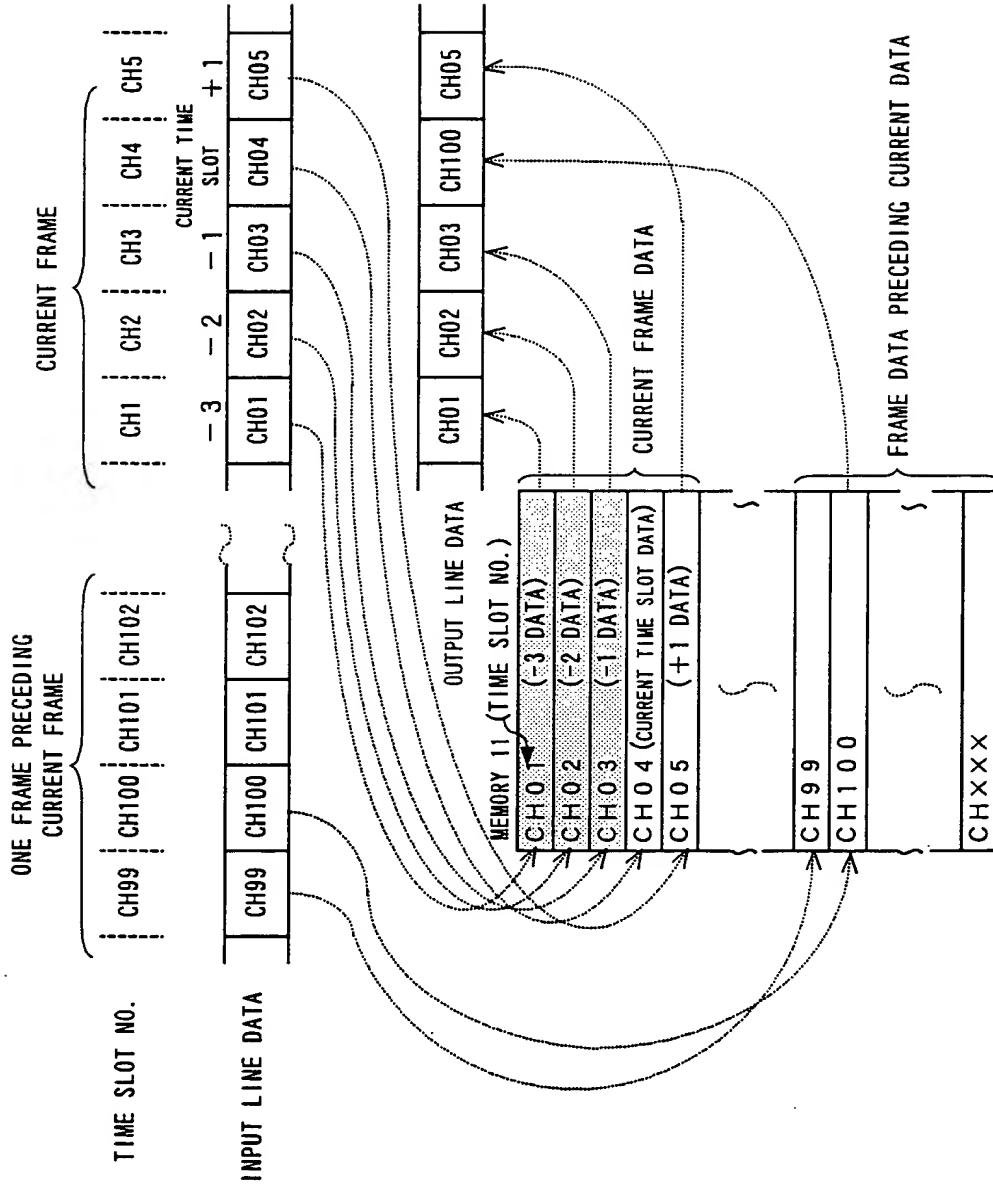


FIG. 8

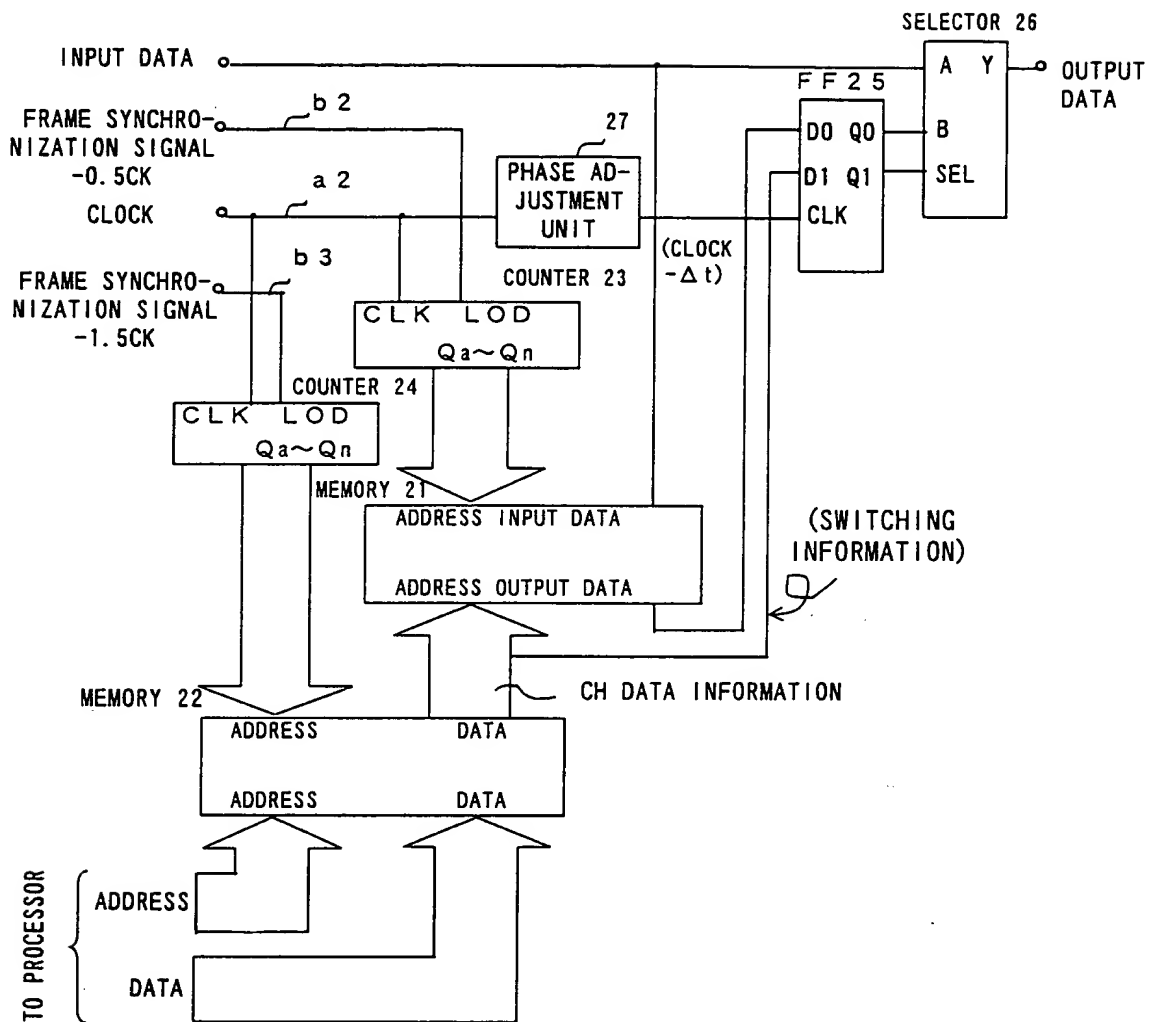


FIG. 9

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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2 2

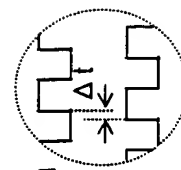
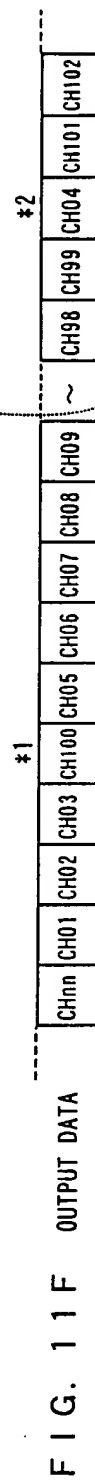
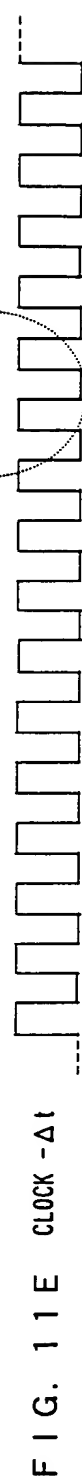
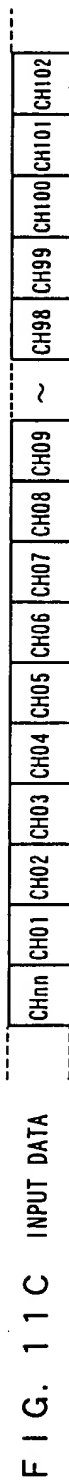
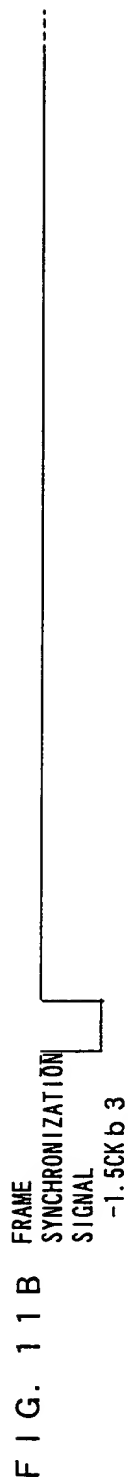


ADDRESS	DATA	
TIME SLOT INFORMATION	(CH DATA INFORMATION)	SWITCHING INFORMATION
0 0 0 0	0 0 0 0	0
0 0 0 1	0 0 0 1	0
0 0 0 2	0 0 0 2	0
0 0 0 3	0 0 0 3	0
0 0 0 4	0 1 0 0	1
0 0 0 5	0 0 0 5	0
}	}	}
0 0 9 9	0 0 9 9	0
0 1 0 0	0 0 0 4	1
0 1 0 1	0 1 0 1	0

* 1

* 2

FIG. 10



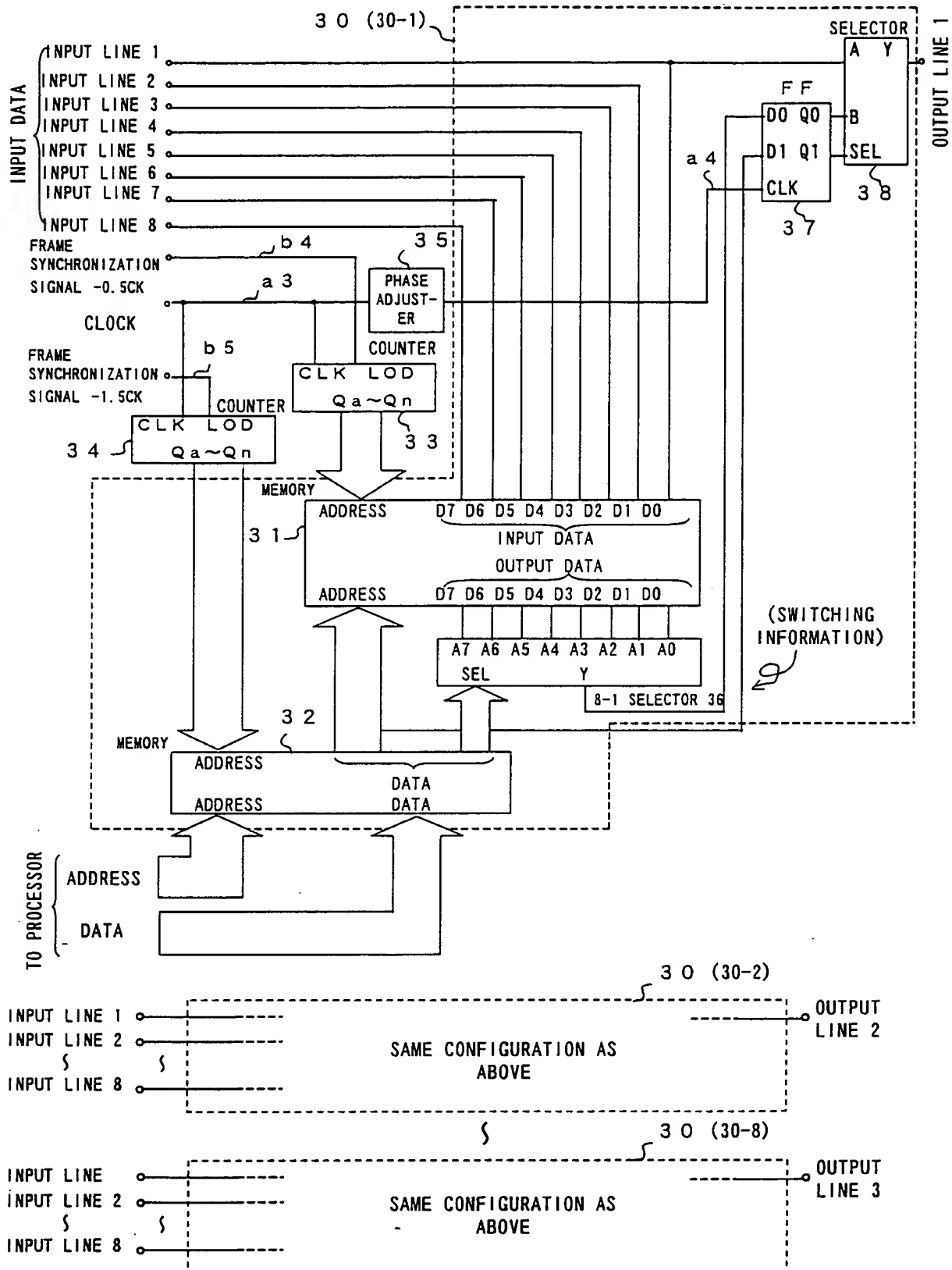


FIG. 12

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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ADDRESS	DATA		
TIME SLOT INFORMATON	CH DATA INFORMATION	LINE INFORMATION	SWITCHING INFORMATION
0 0 0 0	0 0 0 0	0 0	0
0 0 0 1	0 0 0 1	0 0	0
0 0 0 2	0 0 0 2	0 0	0
0 0 0 3	0 0 5 0	0 3	1
0 0 0 4	0 1 0 0	0 0	1
0 0 0 5	0 0 0 5	0 0	0
}	}	}	}
0 0 9 9	0 1 0 0	0 7	1
0 1 0 0	0 0 0 4	0 0	1
0 1 0 1	0 1 0 1	0 0	0

FIG. 13

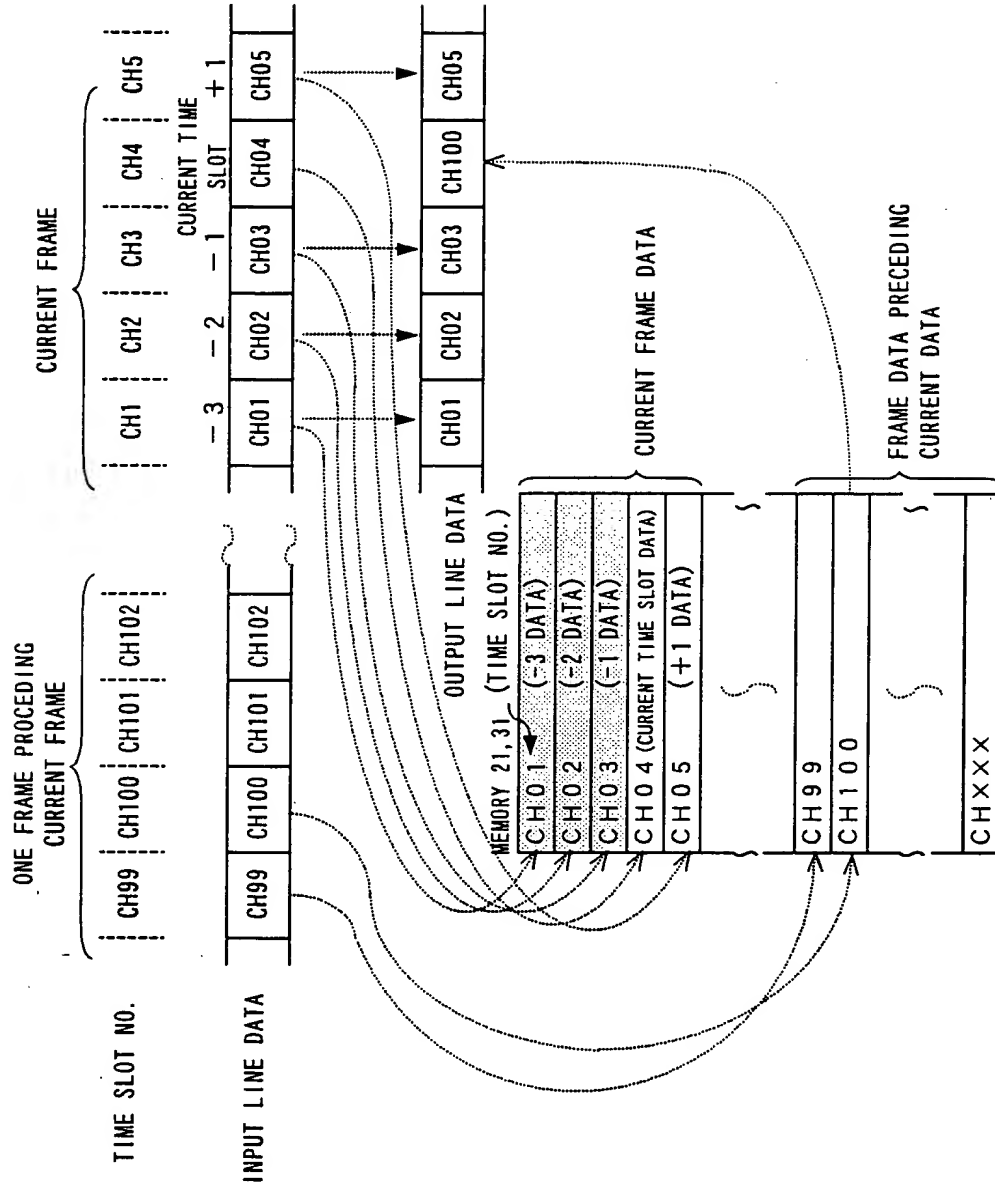


FIG. 14